

FDL Analysis and Emulation

The left screenshot shows the 'FDL Analysis' window with a table of captured frames:

Dev	Msg#	Time	Length	Error	Address	Ctrl	From	Type	Information
2	0	0.00.00.009400	13		x3801	x03 ANS	CI	Performance Report (ANSI T...	x00000001-000
2	1	0.00.00.109400	13		x3801	x03 ANS	CI	Performance Report (ANSI T...	x00000001-000
2	2	0.00.00.209400	13		x3801	x03 ANS	CI	Performance Report (ANSI T...	x00000001-000
2	3	0.00.00.309400	13		x3801	x03 ANS	CI	Performance Report (ANSI T...	x00000001-000
2	4	0.00.00.409400	13		x3801	x03 ANS	CI	Performance Report (ANSI T...	x00000001-000
2	5	0.00.00.509400	13		x3801	x03 A			
2	6	0.00.00.609400	13		x3801	x03 A			
2	7	0.00.00.709400	13		x3801	x03 A			
2	8	0.00.00.809400	13		x3801	x03 A			
2	9	0.00.00.909600	13		x3801	x03 A			
2	10	0.00.01.009400	13		x3801	x03 A			

The right screenshot shows the 'Off-line FDL Protocol Analysis G.704' window with a table of frames:

Dev	TSlot	SubCh	Frame#	TIME (Relative)	Len	Error	Control FDL	Current Status F-bit	Current Status L-bit	Current Status U-bit	EK FD
2	0	0	0	00.00.00.000000	75	x08					
2	0	0	1	00.00.00.211400	37	x08					
2	0	0	2	00.00.00.420400	8	x08		U or L = 1	PLB in Actuated St...	D51 in Available Signal State	
2	0	0	3	00.00.00.620800	225	x08					
2	0	0	4	00.00.00.866000	8	x08		U or L = 1	PLB in Actuated St...	D51 in Unavailable Signal State	
2	0	0	5	00.00.01.071400	213	x08					

Overview

The **Facility Data Link Analyzer (FDL)** is available as a real-time (XX021) as well as offline analyzer (OLV021) application. It can perform FDL decode and analysis on T1 channels in real-time and off-line. The FDLA requires T1 E1 analyzer to be set in ESF mode.

Both real-time and off-line analysis present information in two layers:

- Raw HDLC Frame data as a hexadecimal and ASCII octet dump
- FDL message information

The real-time mode of operation is used to capture stream of HDLC frames and the embedded bit-patterned ESF data link messages on the selected cards. Captured information can be saved to disk for later off-line analysis.

HDLC frames are parsed according to Q.921; FDL messages are decoded based on the AT&T TR54016, ANSI T1.403, T1.408, I.431, G.963 and G.704. Multiple instances of FDL analyzer can run simultaneously capturing data from several T1 lines.

Captured Frames can later be used for traffic simulation using the FDL playback application. The captured bytes can be saved into a file and raw hex bytes being received can be viewed.

For more details, visit [FDL Software for Emulation and Analysis](#) webpage.

Main Features

- Supports Windows® 8.1 and above Operating System with user-friendly GUI
- Real-time as well as off-line analysis
- Summary view displays LAPD information
- Detail view displays decodes of a user selected frame from the Summary View
- Hex dump view displays the frame information in HEX and ASCII format
- Raw HDLC frame data as a hexadecimal and ASCII octet dump
- Capability to export detail decode information to an ASCII



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Summary and Detail Views

FDL Analyzer user interface has two panes: the summary view at the top and the detail view at the bottom. The splitter separating the summary and the detail views allows dividing screen according to user preferences. The summary view columns can be resized and reordered using drag and drop mouse operation. The detail view displays frame summary information, LAPB or LAPD address and control, message details, and hexadecimal dump of all.

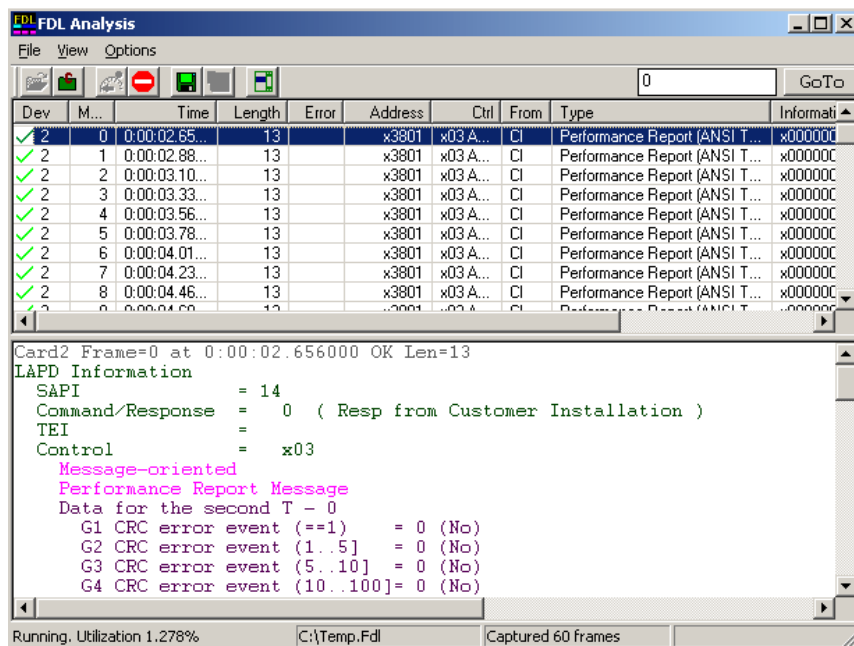


Figure: Summary and Detail View

Facility Data Link Real-time and Off-line Analysis

The real-time analysis is used to capture data on one or multiple T1 lines. The captured data is always stored in a temporary file. Before capturing is started, the capturing options and timeslots must be specified. These settings are saved in the Windows registry and can be reused between FDLA sessions. The off-line mode of operation can be used to analyze a trace saved in a file during real-time analysis.

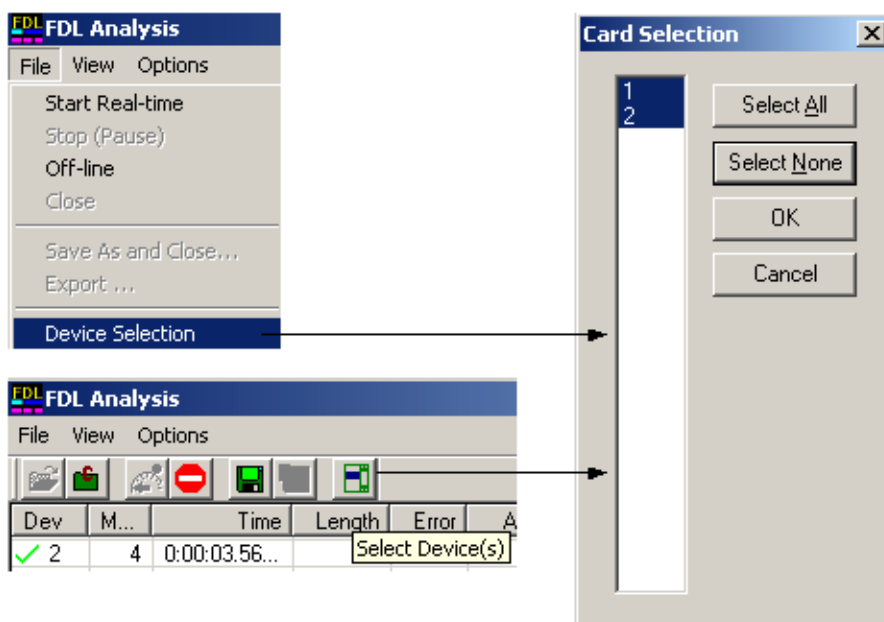


Figure: Data Link Real-time and Off-line Analysis

FDL Playback and View Save Data

The Playback File module of the FDL analyzer allows the user to transmit data directly into the FDL bit stream. The user may transmit valid HDLC frames (properly formed HDLC frame file required), or the user may transmit raw data from a file of raw (hex) bytes (codeword strings, etc.). When transmitting HDLC frames, the number of flags between frames may be selected. The View and Save Data module allows the user to view, the raw hex bytes being received from the FDL bit stream, the Framing bit stream, or the CRC bit stream during real time analysis and to save the received bytes into a file.

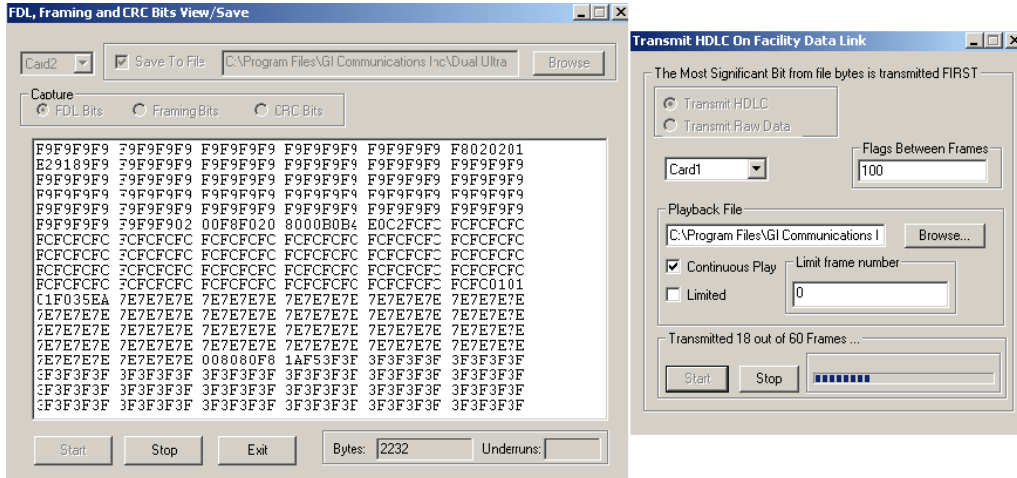


Figure: FDL Playback and View Save Data

FDL Analysis In VB Client (Optional Software)

The FDL utility allows the transmission and reception of FDL messages. FDL messages include the performance report (PRM), the performance report with supplementary performance report (SPRM), the network performance report (NPRM) and the code words. These messages are defined in the ANSI T1.403 specification. The FDL utility window has transmission interface, message definition, PRM monitoring, event list.

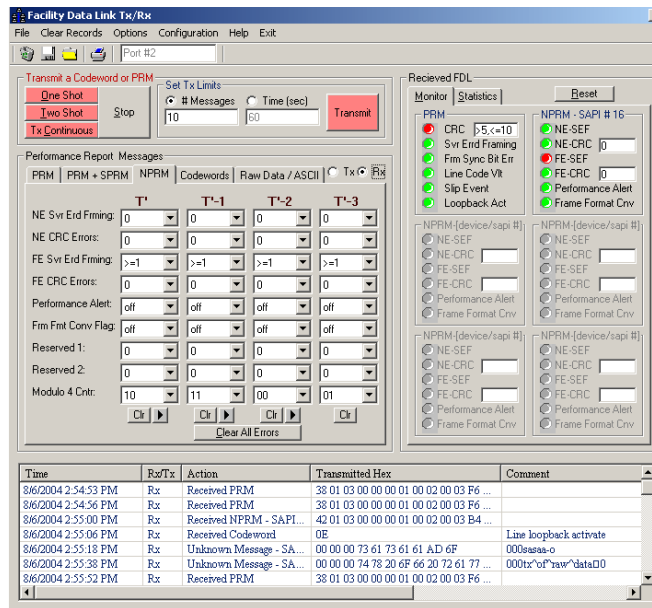


Figure: FDL Playback and View Save Data

Bin2Frm.exe and Frm2Bin.exe Utilities

Bin2Frm.exe utility converts binary input files to HDLC framed output files (.hdl file). It accepts single or multiple binary files as input and assembles them to have a single output HDLC file. Frm2Bin.exe utility accepts a HDLC file input and converts it to a binary file output.

Buyer's Guide

Item No	Product Description
XX021	FDL Software for ESF (T1 only)
OLV021	FDL Offline Analysis (T1 only)

Item No	Related Software
XX660	w/ FDL Option and VB Client to Control FDL

Item No	Related Hardware
PTE001	tProbe™ Dual T1 E1 Laptop Analyzer with Basic Analyzer Software
FTE001	QuadXpress T1E1 Main Board (Quad Port– requires additional licenses)
ETE001	OctalXpress T1E1 Main Board plus Daughter Board (Octal Port– requires additional licenses)
XTE001	Dual T1 E1 Express (PCIe) Boards (requires additional licenses)

For more details, visit [FDL Software for Emulation and Analysis](#) webpage.



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